

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

oplicants:

Y. TAKAHASHI, et al.

Application No.:

10/722,461

Filed:

November 28, 2003

For:

SEMICONDUCTOR MEMORY DEVICE AND

SEMICONDUCTOR INTEGRATED CIRCUIT

Art Unit:

Unknown

Examiner:

Unknown

INFORMATION DISCLOSURE STATEMENT **UNDER 37 CFR '1.97 & 1.98**

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 January 22, 2004

Sir:

In the matter of the above-identified application, applicants are submitting herewith copies of documents listed in the attached form PTO/SB/08A for the Examiner's consideration.

This information disclosure statement is being submitted within three months of the filing date.

To the extent the documents listed on the attached form PTO/SB/08A are not in the English language, the requirement of 37 CFR '1.98(a)(3) for a concise explanation of the relevance is satisfied by the submission of an English language abstract attached thereto and discussed in the specification.

It is respectfully requested that this information disclosure statement be considered by the Examiner.

Please charge any shortage in the fees due in connection with the filing of this paper, including extension of time fees, to the deposit account of Antonelli, Terry, Stout & Kraus, LLP, Deposit Account No. 01-2135 (501.43253X00) please credit any excess fees to such deposit account.

Respectfully submitted,

ANTONELLI, TERRY, STOUT & KRAUS, LLP

Gregory E. Montone

Reg. No. 28,141

GEM/dlt

1300 North Seventeenth Street, Suite 1800

Arlington, Virginia 22209 Telephone: (703) 312-6600

Facsimile: (703) 312-6666

PTO/SB/08A (10-01)

Approved for use through 10/31/2002. OMB 0651-0031

U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

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Substitute for form 1449A/PTO				Complete if Known		
				Application Number	10/722,461	
	INFORMATION DISC	CLOSU	JRE	Filing Date	November 28, 2003	
	STATEMENT BY AP	PLICA	ANT	First Named Inventor	Y. TAKAHASHI, et al.	
				Art Unit	Unknown	
	(use as many sheets as n	ecessar	v)	Examiner Name	Unknown	
Sheet	1	of	1	Attorney Docket Number	501.43253X00	

			U.S. PATENT I	DOCUMENTS	
Examiner Initials'	Cite No.1	Document Number	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)	MM-DD-YYYY		
		5,771,190	06-1998	Okamura	
-		2001/0033511 A1	10-2001	Saito et al.	
-		2002/0003244 A1	01-2002	Tooher et al.	
		6.344,992 B1	02-2002	Nakamura	
-				<u> </u>	
				 	
				 	

		FOREIG	N PATENT DO	CUMENTS		
Examiner Initials'	Cite No. ¹	Foreign Patent Document Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
	-	JP 04-168694 JP 07-057476	06-1992 03-1995	Mitsubishi Electric Corp NEC Corp		x

		OTHER PRIOR ART—NON PATENT LITERATURE DOCUMENTS	_				
Examiner Initials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published					
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^{*}EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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